

Amendments to the Drawings:

The attached sheets of drawings include changes to Figs. 4 to 6. In Figs. 4 to 6, reference numerals 11 and 19 have been added to identify conductors that form a coupler.

Attachment: Replacement Sheets

Annotated Sheet Showing Changes

REMARKS

Claims 1, 2, and 4 were pending in the present application when last examined. Applicant has amended claims 1, 2, and 4, and added claims 5 and 6. Claims 1, 2, 4 to 6 remain pending in the present application.

Amendments to the Specification and the Drawings

Applicant has amended the Specification and the Drawings to correct inconsistencies between them. Applicant respectfully requests the Examiner to review and enter these amendments.

Objections to the Drawings

The Examiner objected to the drawings because they do not show a conductor and a coupler as previously recited in claim 1. Applicant has amended claim 1 to recite first and second conductors that form a coupler. Thus, claim 1 now recites what is shown in Figs. 4, 5, and 6.

The Examiner objected to the drawings because they do not show a conductor "above" a semiconductor substrate since the Examiner believes the drawings are electrical schematics and not structural drawings. Applicant notes that the present application does not specify Figs. 4, 5, and 6 as electrical schematics. Thus, Figs. 4, 5, and 6 must be interpreted from the viewpoint of one skilled in the art. Figs. 4, 5, and 6 illustrate a conductor (now labeled 19) external to a substrate 18. Thus, conductor 19 is not part of substrate 18. One skilled in the art knows that integrated circuits are built by forming components on top of a substrate (e.g., depositing metal on a substrate to form a conductor). Thus, one skilled in the art would naturally interpret conductor 19 as being formed on, thereby being above, substrate 18. Furthermore, Applicant notes that Figs. 4, 5, and 6 literally illustrate conductor 19 above substrate 18. Accordingly, Applicant respectfully requests the Examiner to withdraw his objections to the drawings.

Objections to the Claims

As the Examiner suggested, Applicant has amended the claims to use a consistent term of "semiconductor substrate." Applicant respectfully requests the Examiner to withdraw his objections to the claims.

§ 112 Rejections

As the Examiner correctly pointed out, a coupler may be formed by two conductors. Accordingly, Applicant has amended claim 1 to recite two conductors that form a coupler. Furthermore, Applicant has discussed above that one skilled in the art would interpret the drawings to show one of the two conductors as being above the semiconductor substrate. Accordingly, Applicant respectfully requests the Examiner to withdraw the § 112 rejections.

§ 102 Rejections

The Examiner rejected claims 1, 2, and 4 under 35 U.S.C. § 102(b) as being anticipated by “Monolithic GaAs Interdigitated Couplers” by Kumar et al. (“Kumar et al.”). Applicant has amended claim 1, which now recites:

1. A circuit, comprising:

a semiconductor substrate, comprising:

a first conductor;

a detector electrically connected to the first conductor;

a second conductor above the substrate and aligned with the first conductor, wherein the first and the second conductors form a coupler that detects a power delivered into the second conductor.

Amended claim 1. Kumar et al. does not disclose or suggest a semiconductor substrate with a first conductor and a detector, and a second conductor above the semiconductor substrate and aligned with the first conductor as recited in amended claim 1.

Claims 2 and 4 depend directly or indirectly from amended claim 1 and are patentable over the cited references for at least the same reasons as amended claim 1.

New Claims

New claims 5 and 6 depend from amended claim 1 and are patentable over the cited references for at least the same reasons as amended claim 1.

New claim 6 recites similar claim elements previously found in canceled claim 3, which the Examiner objected to in the October 12, 2005 Office Action. Specifically, the Examiner requested additional information to determine if one of ordinary skill in the art can reasonably construct a charge pump circuit as shown in Fig. 6 at the filing of the present application. Applicant hereby responds to the request for additional information.

A charge pump (of a form also known as a voltage multiplier) has a port for AC power, a ground connection, and an output. For simplicity, the port for the AC power has been omitted in Fig. 6. One skilled in the art knew how to build a charge pump at the time the present application was filed. For support, Applicant concurrently submits a § 1.132 Declaration from the inventor Michael L. Frank.

Summary

In summary, claims 1, 2, and 4 were pending in the above-identified application. Applicant has amended claims 1, 2, and 4, and added claims 5 and 6. For the above reasons, Applicant respectfully requests the Examiner to withdraw the claim objections/rejections and allow claims 1, 2, 4 to 6. Should the Examiner have any questions, please call the undersigned at (408) 382-0480x206.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.


Signature
6/7/06
Date

Respectfully submitted,



David C. Hsia
Attorney for Applicant(s)
Reg. No. 46,235